

Data in process image

			Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
Valid, if the coupling module parameter byte alignment is disabled (default) and byte n has been used halfway. Up to 8 bit input data are mapped.	Input	Byte n (M8)	C3P4	C2P4	C1P4	C0P4	is used by the physically preceding bit-oriented extension module connected via the IP Link.			
		Byte n (M12)	C1P2	C1P4	C0P2	C0P4				
		Byte n+1 (M8)	is used by the physically following bit-oriented extension module connected via the IP Link.				C7P4	C6P4	C5P4	C4P4
		Byte n+1 (M12)					C3P2	C3P4	C2P2	C2P4
Valid, if the coupling module parameter byte alignment is active or disabled (default) and the previous byte has been completely used. Up to 8 bit input data are mapped.	Input	Byte n (M8)	C7P4	C6P4	C5P4	C4P4	C3P4	C2P4	C1P4	C0P4
		Byte n (M12)	C3P2	C3P4	C2P2	C2P4	C1P2	C1P4	C0P2	C0P4

C... = Connector no. – P... = Pin no.